

data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

an accessed buffer memory switch means for switching between said two buffer memories in order to read or write data as a target of error
5 correction alternately in units of said predetermined capacity.

49. The error correction device of claim 26 further comprising:

two buffer memories each having a predetermined capacity equivalent to one sector or one ECC block;

10 a buffer memory storage means for alternately storing in said two buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

15 an accessed buffer memory switch means for switching between said two buffer memories in order to read or write data as a target of error correction alternately in units of said predetermined capacity.

50. The error correction device of claim 27 further comprising:

two buffer memories each having a predetermined capacity
20 equivalent to one sector or one ECC block;

a buffer memory storage means for alternately storing in said two buffer memories, in accordance with error correction speed, continuous data of the predetermined capacity which are a target of error correction and have been read from a DVD or a CD-ROM; and

25 an accessed buffer memory switch means for switching between said

two buffer memories in order to read or write data as a target of error correction alternately in units of said predetermined capacity.

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